

[FIG.1]

ANTENNA #1

101-1 RECEPTION PROCESSING SECTION

102-1 BUFFER

5 ANTENNA #2

101-2 RECEPTION PROCESSING SECTION

102-2 BUFFER

104 CONTROL SECTION

105 DESPREADING SECTION

10 106 DELAY PROFILE GENERATION SECTION

107 PATH DETECTION SECTION

108 DESPREADING SECTIONS

109 RAKE COMBINING SECTION

110 DECODING SECTION

15 RECEIVE DATA

[FIG.2]

FRAME

SLOT

20 TOTAL: N CELLS

ANTENNA #1

PATH DETECTION

[FIG.3]

25 FRAME

SLOT

TOTAL: UP TO N CELLS

ANTENNA #1

ANTENNA #2 (HSDPA SERVING CELL ONLY)
PATH DETECTION

[FIG.4]

5 ANTENNA #1
101-1 RECEPTION PROCESSING SECTION
102-1 BUFFER
ANTENNA #2
101-2 RECEPTION PROCESSING SECTION
10 102-2 BUFFER
104 CONTROL SECTION
105 DESPREADING SECTION
106 DELAY PROFILE GENERATION SECTION
107 PATH DETECTION SECTION
15 108 DESPREADING SECTIONS
109 RAKE COMBINING SECTION
110 DECODING SECTION
RECEIVE DATA
117 GAIN CONTROL SECTION
20 118 CONTROL SECTION
120 CELL SEARCH SECTION
CELL SEARCH RESULT

[FIG.5]

25 ANTENNA #1
101-1 RECEPTION PROCESSING SECTION
102-1 BUFFER
ANTENNA #2

101-2 RECEPTION PROCESSING SECTION
102-2 BUFFER
104 CONTROL SECTION
105 DESPREADING SECTION
5 106 DELAY PROFILE GENERATION SECTION
107 PATH DETECTION SECTION
108 DESPREADING SECTIONS
109 RAKE COMBINING SECTION
110 DECODING SECTION
10 RECEIVE DATA

[FIG.6]

FRAME

SLOT

15 TOTAL: N+1 CELLS
ANTENNA #1
ANTENNA #2 (HSDPA SERVING CELL ONLY)
PATH DETECTION

20 [FIG.7]

ANTENNA #1

101-1 RECEPTION PROCESSING SECTION

102-1 BUFFER

ANTENNA #2

25 101-2 RECEPTION PROCESSING SECTION
102-2 BUFFER
104 CONTROL SECTION
105 DESPREADING SECTION

106 DELAY PROFILE GENERATION SECTION
 107 PATH DETECTION SECTION
 108 DESPREADING SECTIONS
 109 RAKE COMBINING SECTION
 5 110 DECODING SECTION
 RECEIVE DATA

[FIG.8]

FRAME

10 SLOT

TOTAL: N+2 CELLS

ANTENNA #1

ANTENNA #2 (HSDPA SERVING CELL + UL SERVING CELL)

PATH DETECTION

15

[FIG.9]

ANTENNA #1

101-1 RECEPTION PROCESSING SECTION

102-1 BUFFER

20 ANTENNA #2

101-2 RECEPTION PROCESSING SECTION

102-2 BUFFER

104 CONTROL SECTION

105 DESPREADING SECTION

25 106 DELAY PROFILE GENERATION SECTION

107 PATH DETECTION SECTION

114 DESPREADING SECTIONS

115 DESPREADING SECTIONS

116 BUFFER
109 RAKE COMBINING SECTION
110 DECODING SECTION
RECEIVE DATA

5

[FIG.10]
FRAME
SLOT
TOTAL: N+1 CELLS

10 ANTENNA #1
ANTENNA #2 (MBMS CELL ONLY)
PATH DETECTION

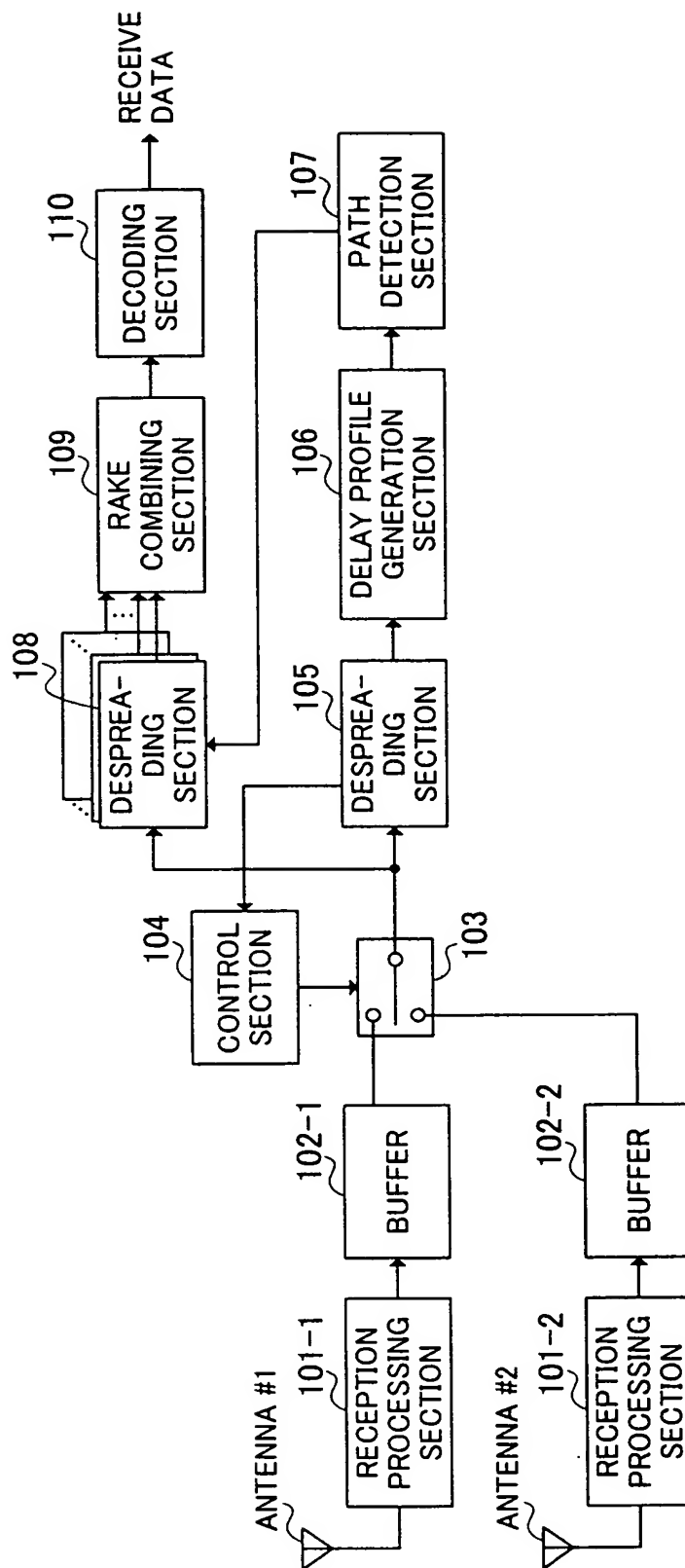


FIG.1

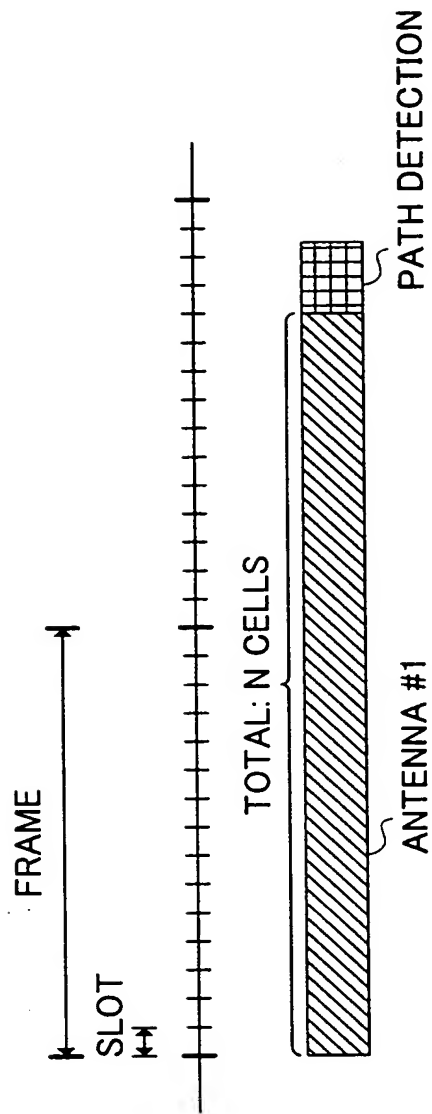


FIG.2

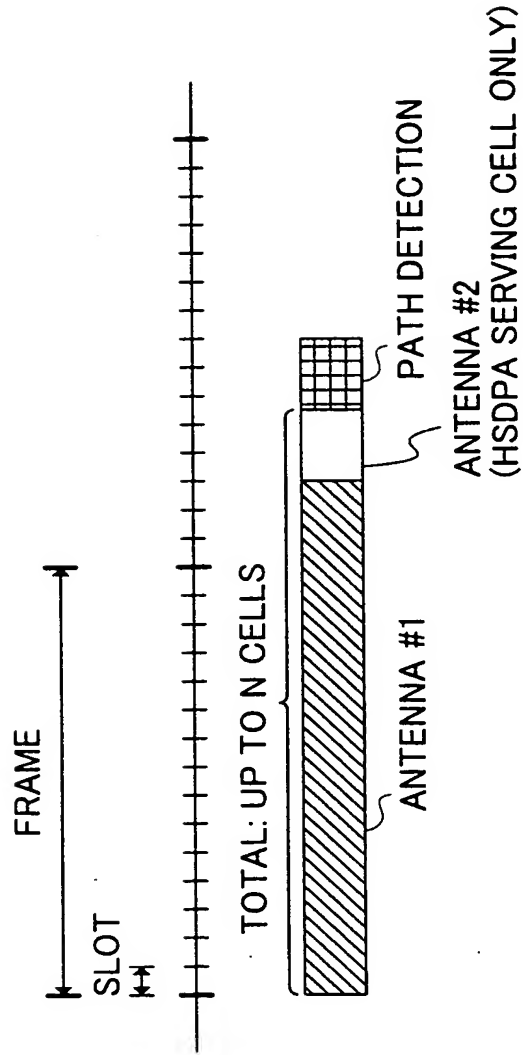


FIG.3

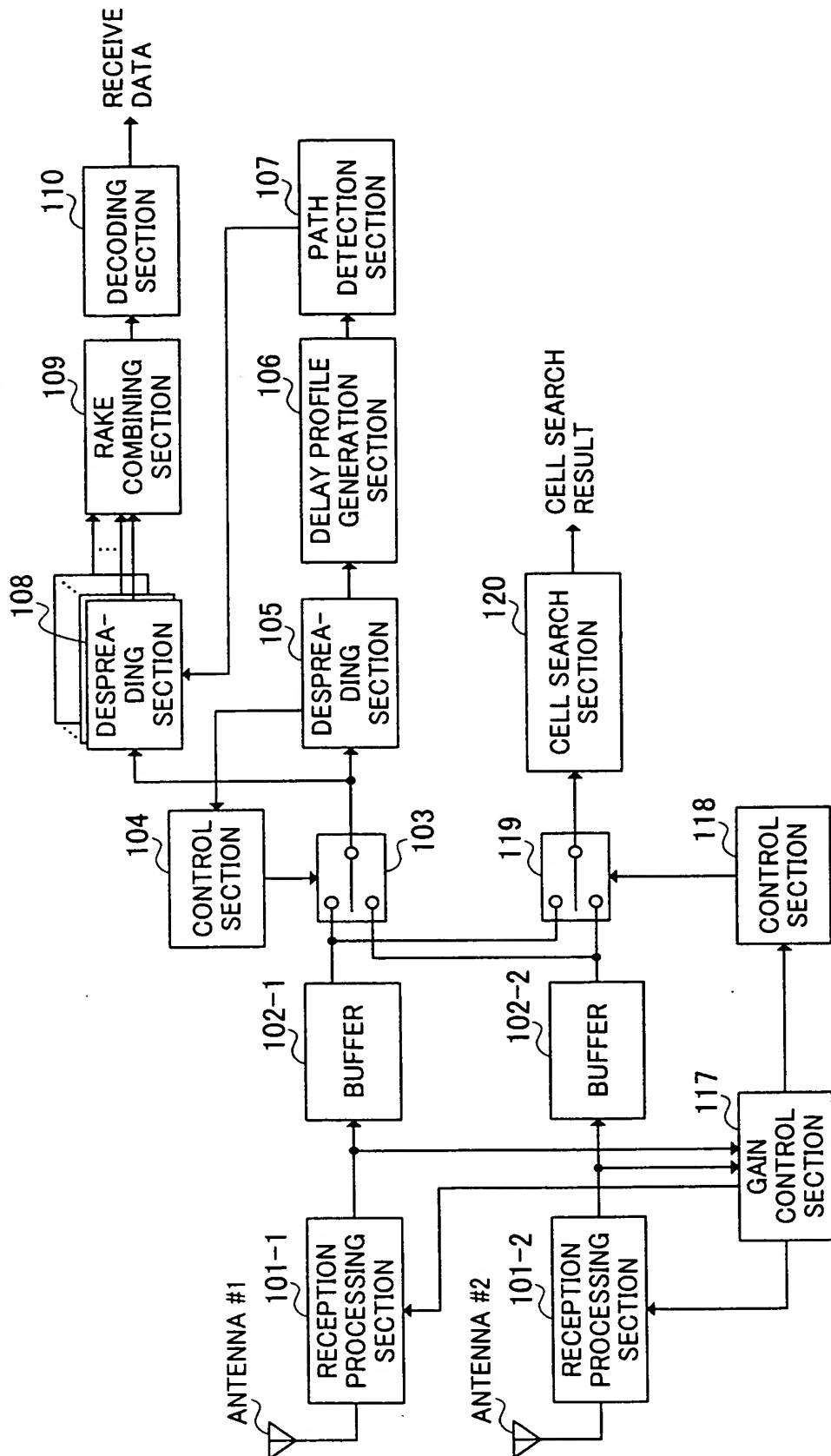


FIG.4

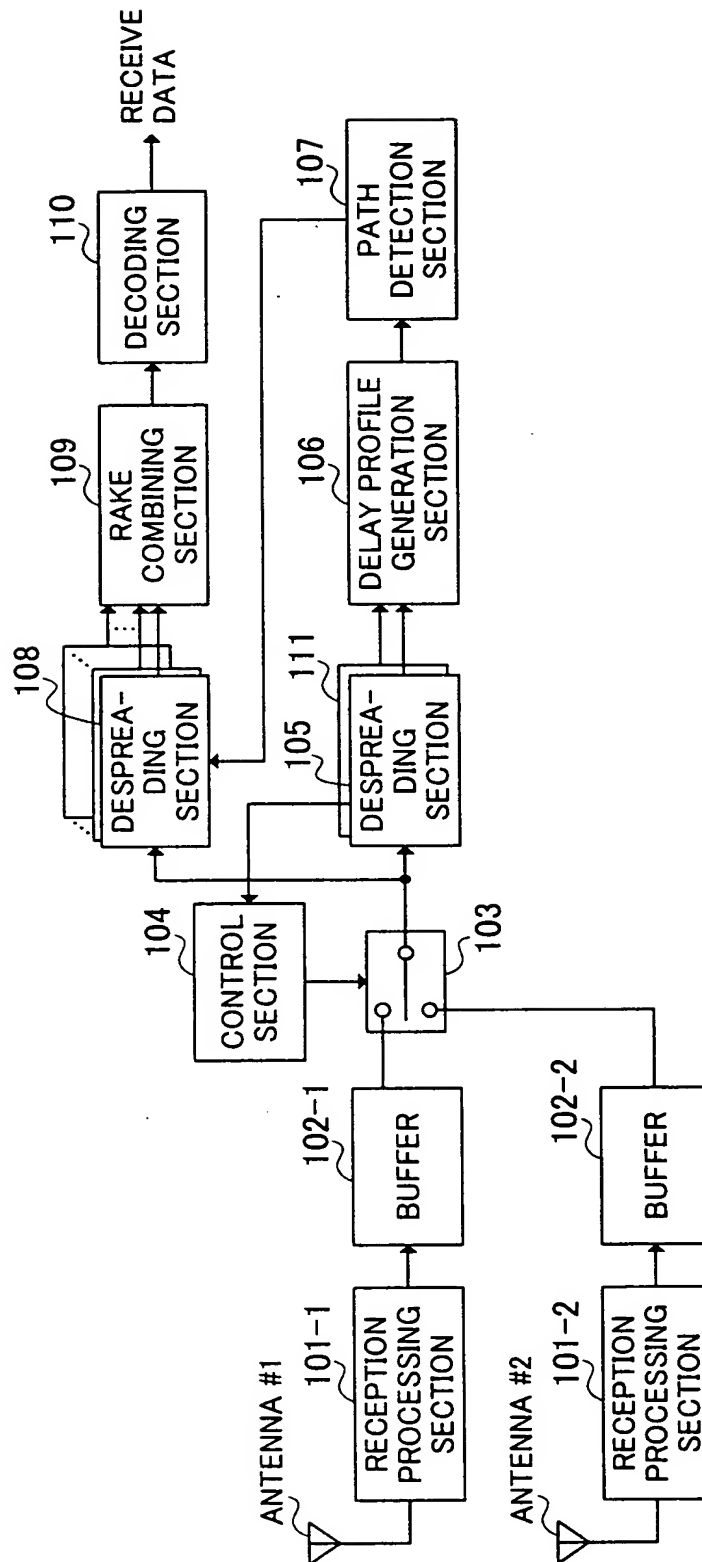


FIG.5

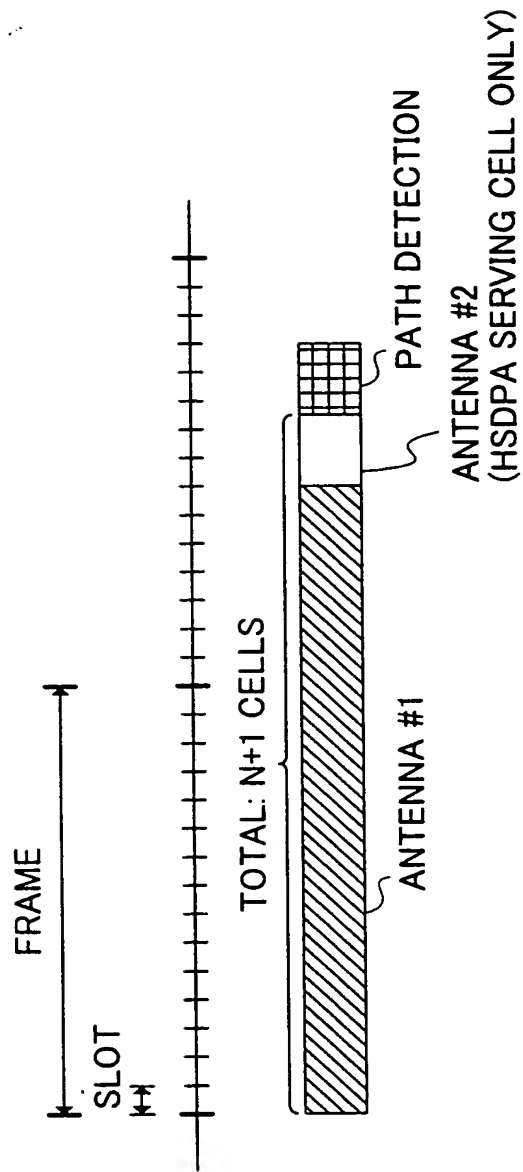


FIG.6

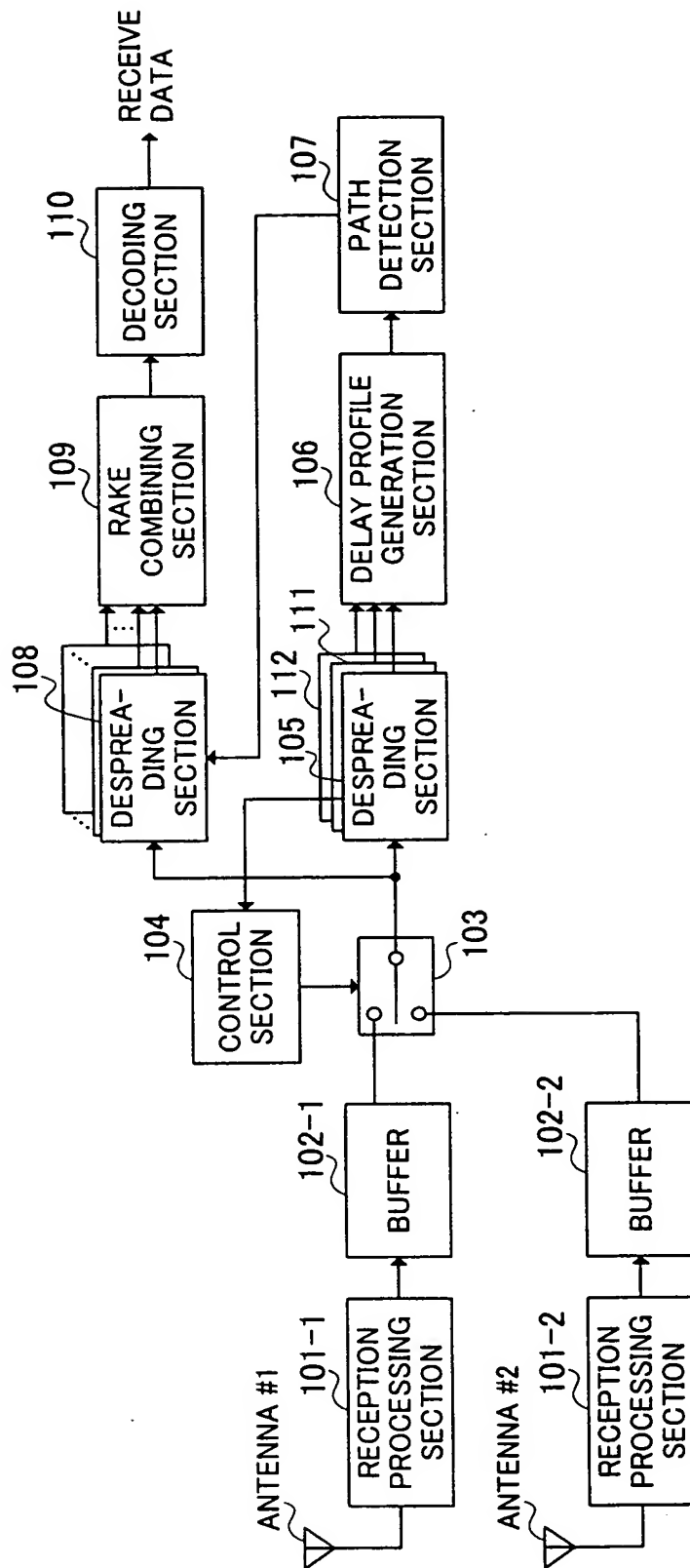


FIG. 7

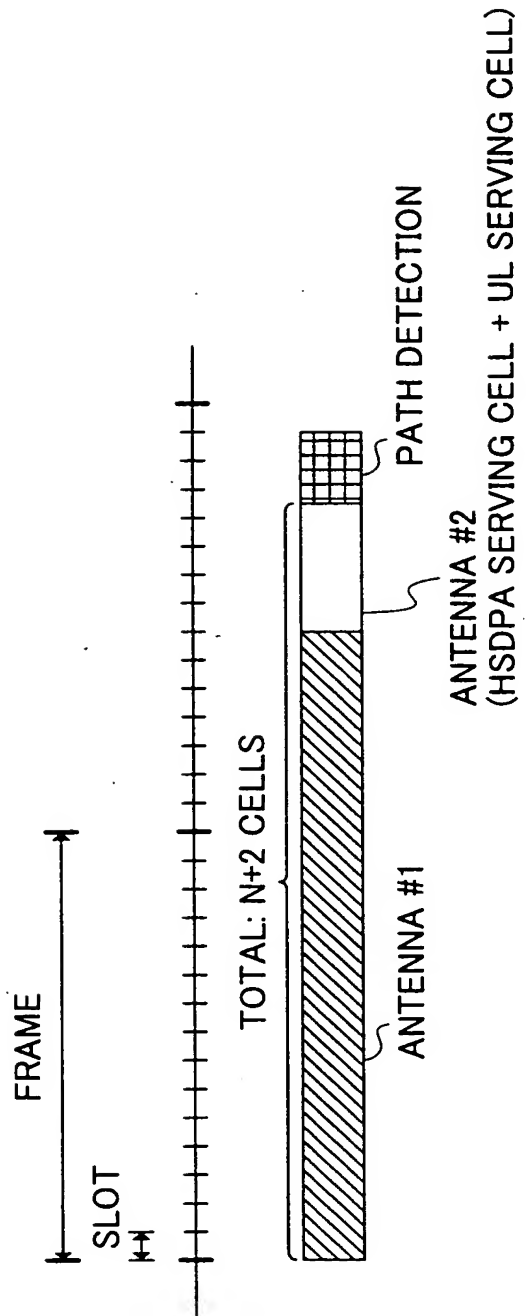


FIG.8

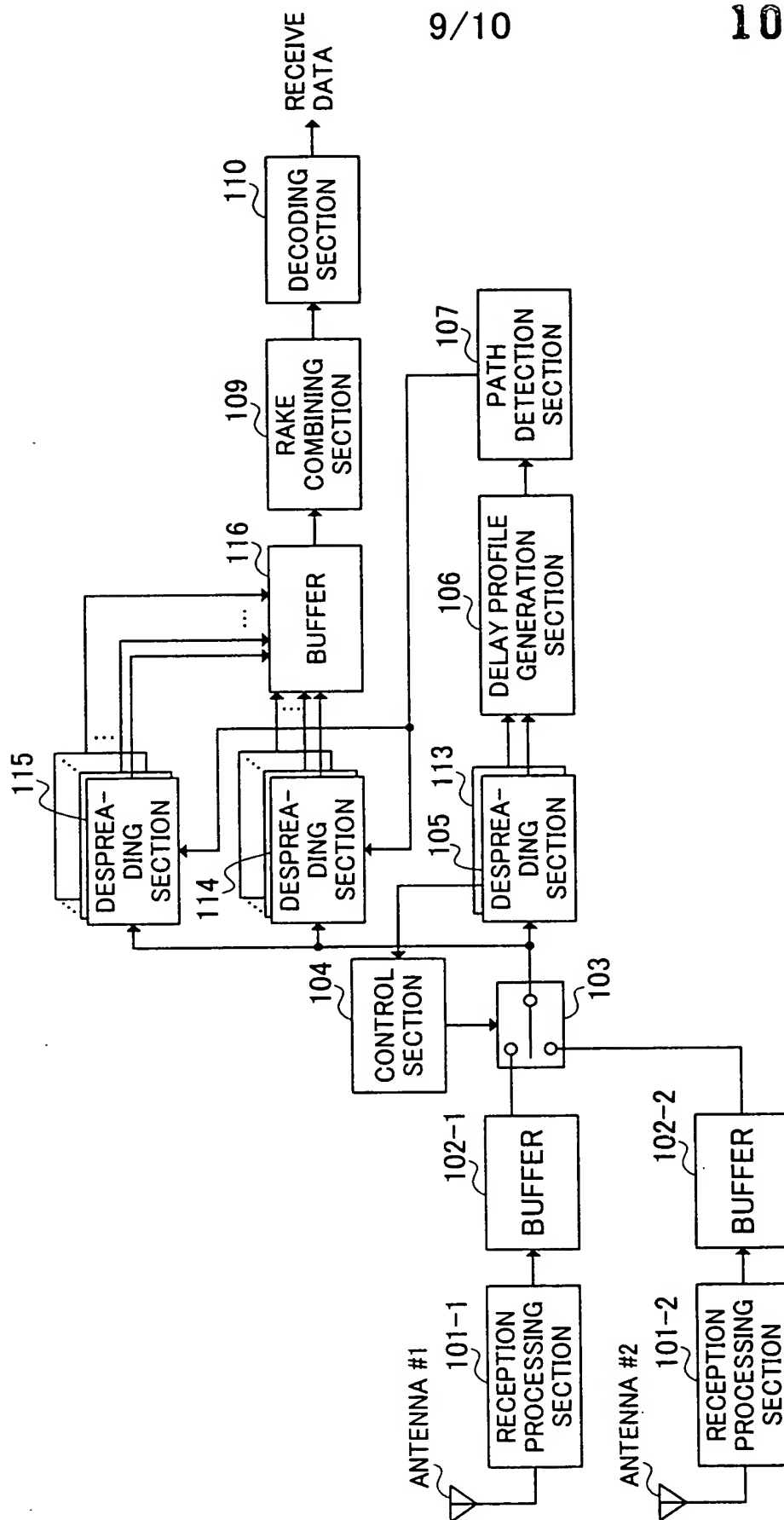


FIG. 9

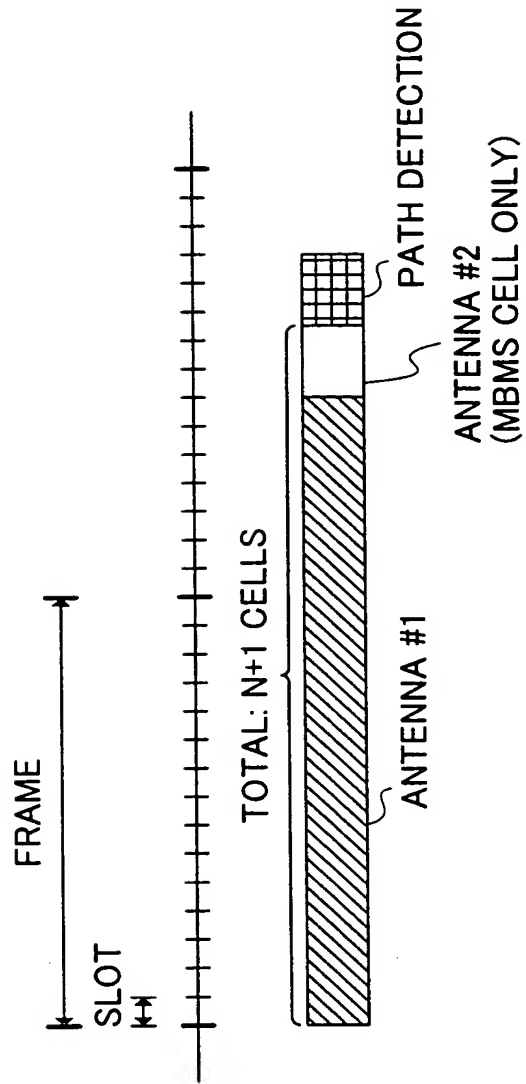


FIG.10